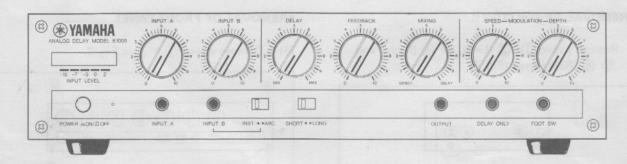
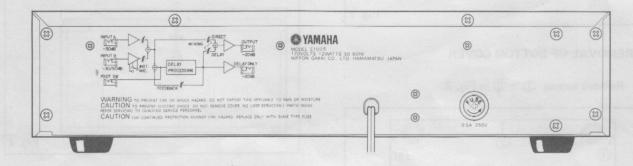


SERVICE MANUAL

FRONT PANEL



MREAR PANEL





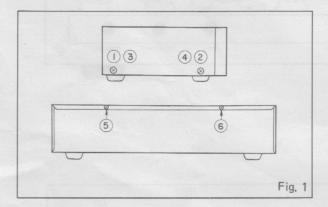
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IDISASSEMBLY PROCEDURES

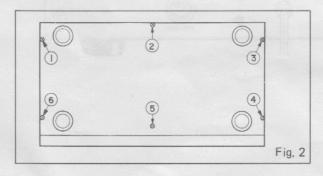
•REMOVAL OF TOP COVER

Remove screws ①, ② (Left), ③ ④ (Right) and ⑤ ⑥ (Rear.Panel) (Fig. 1)



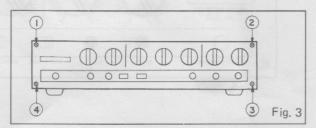
•REMOVAL OF BOTTOM COVER

Remove screws 1 ~ 6 in Fig. 2.

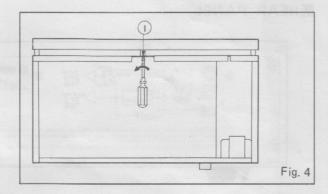


•REMOVAL OF FRONT PANEL

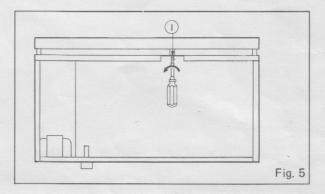
(1) Remove screws \bigcirc \sim \bigcirc in Fig. 3.



(2) Remove screw 1 in Fig. 4.



(3) Remove screw 1 in Fig. 5.



MADJUSTMENT

ADJUSTING METHOD

As the test points are on the pattern-side, adjustment are performed from the pattern side (i.e., with the cover opened) as a rule.

■INSTRUMENTS USED FOR ADJUSTMENT

1. Low frequency oscillator: The sine wave is used.

2. Oscilloscope: Capable of measuring

the time axis (X-axis).

3. Milli-voltmeter: It is suggested that

two sets be used, one for input and the other for output.

(dBm scale)

4. Frequency counter: Should be able to

measure periods. (Extra-low frequency

measurements)

5. Distortion factor gauge: 400Hz

6. Tester: For DC voltage

measurements.

7. Filter: 12.47kHz, -6dB/oct.

BASIC SETTING

Unless specified otherwise, the controls and switches should be set as shown below. This setting is referred to as the basic setting.

INPUT A, INPUT B & DELAY 10 (Max.) FEEDBACK, MODULATION (SPEED, DEPTH) MIXING DIRECT

1. CONFIRMATION OF POWER SUPPLY VOLTAGE

The power supply voltage should be within the ranges shown below.

- Between terminals +15V (TP18) and E (TP17)
-+15V±1V Between terminals -15V (TP19) and E (TP17)
- Between terminals +8V (TP20) and E (TP17)

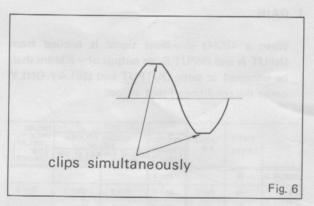
..... +8V±1V

2. CLOCK FREQUENCY ADJUSTMENT (at 2 points)

- Connect the frequency counter to TP11.
- With the SHORT/LONG switch positioned to the LONG side, Adjust VR2 so that the frequency will be 10.7±3kHz. Then, turn the switch over to the SHORT side and adjust VR3 so as to bring the frequency within 27.3±0.3kHz.

3. BBD BIAS ADJUSTMENT

- Connect the low frequency oscillator to the INPUT B terminal and the oscilloscope to TP12 (IC15 OUT).
- With the LEVEL switch set to INST., apply a 400Hz sine wave and gradually increase the output of the oscillator. While observing the oscilloscope, adjust VR4 so that the upper and lower waveforms will clip simultaneously.
- Next, connect the oscilloscope to TP13 (IC16) OUT), and perform adjustment in the same manner. using VR5.



4. BBD OUTPUT BALANCE ADJUSTMENT

This adjustment is carried out to minimize leakage of the clock components of the BBD IC output at TP13 (IC16 OUT) and TP14 (IC17 OUT) during nosignal input.

Connect the oscilloscope to TP13.

Turn the INPUT A and INPUT B controls down to minimum.

Adjust VR6 so that the clock waveform will become the smallest.

Then, connect the oscilloscope to TP14, and adjust in the same way, using VR7.

5. GAIN ADJUSTMENT

Connect the low frequency oscillator and millivoltmeter to the INPUT B terminal, and a $10k\Omega$ load resistor and milli-voltmeter to TP9 (DELAY ONLY terminal).

With the LEVEL switch set to INST, apply a sinewave signal of 400Hz, -30dBm. Setting the SHORT/LONG switch to LONG, adjust VR8 so as to make the output level -20dB±0.5dBm.

Next, turn the switch over to SHORT, and adjust VR9 so as to bring the output level similarly to $-20 \, \mathrm{dB} \pm 0.5 \, \mathrm{dBm}$.

6. LED LIGHTING LEVEL ADJUSTMENT

Under the same input conditions as "5", adjust VR1 so that, of the 5 LEDs arranged in series, 4 of them will illuminate one by one from the first one.

CONFIRMATION OF ELECTRICAL PERFORMANCE

After adjustments have been completed, confirm that the following electrical performances can be obtained.

1. GAIN

When a 400Hz sine-wave signal is applied from INPUT A and INPUT B, an output of -20dBm shall be obtained at both OUTPUT and DELAY ONLY under the conditions listed below.

	9-18-1			OUTPUT	DELAY	
	INPUT LEVEL	INPUT	MIXING	DE	VOLUME LAY	(DELAY)
		SW.	VOLUME	DELAY SW. SHORT	DELAY SW. LONG	SW. SHORT
INPUT A	-50		-20 ± 2			
	- 30	INST	-20 ± 2	-20 + 2	-20 ± 2	-20 ± 2
INPUT B	-50	MIC	-20 ± 2			

2. FREQUENCY RESPONSE

2-1 DIRECT Frequency Response

When input signals the same as those shown in Table 1 are applied to INPUT A and INPUT B, the output frequency response obtained at the OUTPUT terminal shall be within the range of 0 ± 1.5 dB for between $50\sim10$ kHz, when 400Hz is taken as the reference level.

2-2 DELAY Frequency Response

With the set in the condition of "2-1", turn the MIXING control over to DELAY and apply a -30dBm sine-wave signal to INPUT B (INST). The output frequency response obtained at the OUT-PUT terminal shall be within the range shown in

the following tables, when 400Hz is taken as the reference level.

When DELAY SW. is set to SHORT

50Hz	5kHz	
-1.0±2	-0.5±2	
Tab	le 2 Unit :	

When DELAY SW, is set to LONG

50Hz	1kKz
-1.0 ± 2	-1.8 ± 2
Tabl	e-3 Unit:d

3. MAXIMUM OUTPUT

When a 400Hz sine-wave signal is applied from INPUT B (INST.) with the set in the basic setting, the total harmonic distortion factor (T.H.D.) shall be no more than 0.5% when the output level of OUTPUT is —3dBm. From this condition, turn the MIXING control to DELAY. The T.H.D. should be no more than 3% when the SHORT/LONG switch is positioned to SHORT, and no more than 5% when positioned to LONG.

In addition, the output waveform shall be free of such abnormalities as fragmented portions, or oscillation.

4. DELAY TIME

With the set in the basic-setting condition, and the MIXING control positioned to DELAY, delay time should be within the ranges listed in the following table.

DELAY VOL. DELAY SW.	MIN	MAX
SHORT	4ms, or less	9.3mS±5%
LONG	70ms, or less	380mS±5%

Table-4

5. NOISE LEVEL

5-1 DIRECT

Put the set in the condition of "basic setting". When the INPUT A and INPUT B terminals are snortcircuited, the noise level should be no more than the values given in the following table under the conditions given therein.

INPUT A VOLUME	MAX	MAX	MIN	MIN	MIN
INPUT BVOLUME	MAX	MIN	MAX	MAX	MIN
INPUT B SW.	MIC		MIC	INST	
NOISE LEVEL	-81	-84	-84	-90	-92

Table 5 Unit: dB(0dB=0.775Vrms)

When measuring the noise level, a 12.47kHz, -6dB/ oct filter for compensation is to be used.

5-2 DELAY

With the set in the condition of 5-1, turn the INPUT A control down to minimum, set the MIXING control to DELAY, the INPUT B switch to the INST-side and change the SHORT/LONG switch over from SHORT to LONG. The noise level in this condition should be no more than -90 dBm.

6. LFO FREQUENCY

The oscillating frequency that appears at the TP15 terminal shall be within the range of 0.07~0.3Hz when the SPEED control is turned down to mini-

mum, and within $10\sim25$ Hz when turned up to maximum,

7. FET SWITCH

With the set in the basic-setting condition, apply a 400Hz, -30dBm sine wave from INPUT B (INST). When the FET switch is forced to turn off through the short-circuiting of the TP16 terminal, the output level appearing both at OUTPUT and DELAY ONLY should be -60dBm, or less.

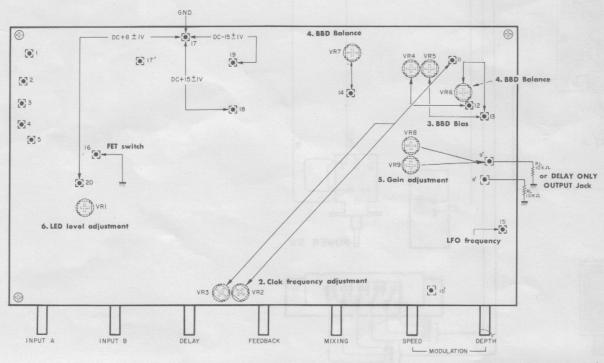
8. FOOT SWITCH

When, under the input conditions similar to those of "7", the MIXING control is set to DELAY and the FOOT SW to ON (or TP10 is short-circuited), the output level of both OUTPUT and DELAY ONLY should be -60dBm, or less.

9. POWER INDICATOR

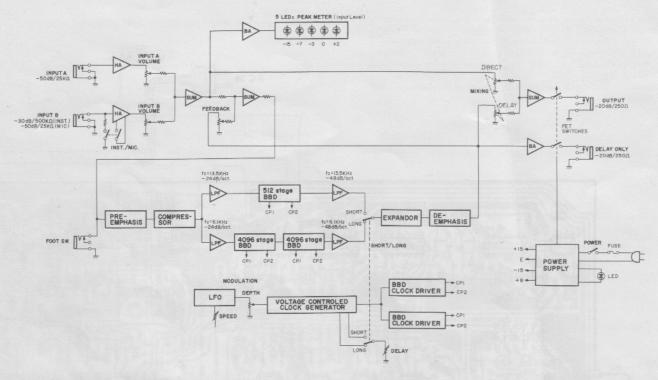
The POWER indicator LED should light up.

MADJUSTING POINTS



Note: Adjusting points on circuit Board as viewed fom the pattern side.

BLOCK DIAGRAM



SPECIFICATIONS

INPUT TERMINALS

CONNECTION	ACTUAL LOAD	FOR USE W/	SENSITIVITY	INPUT LEVEL		
CONTRACTION	IMPEDANCE	NOMINAL		NOMINAL	MAX. BEFORE CLIP	
INPUT A	25kΩ	150 to 5kΩ MICROPHONE	-50dB	-50dB	-22dB	
INPUT B INST.	500kΩ	5kΩ LINE & INSTRUMENT	-30dB	-30dB	-2dB	
INPUT B MIC.	25kΩ	150 to 5kΩ MICROPHONE	-50dB	-50dB	-22dB	

^{* 0}dB= 0.775Vrms

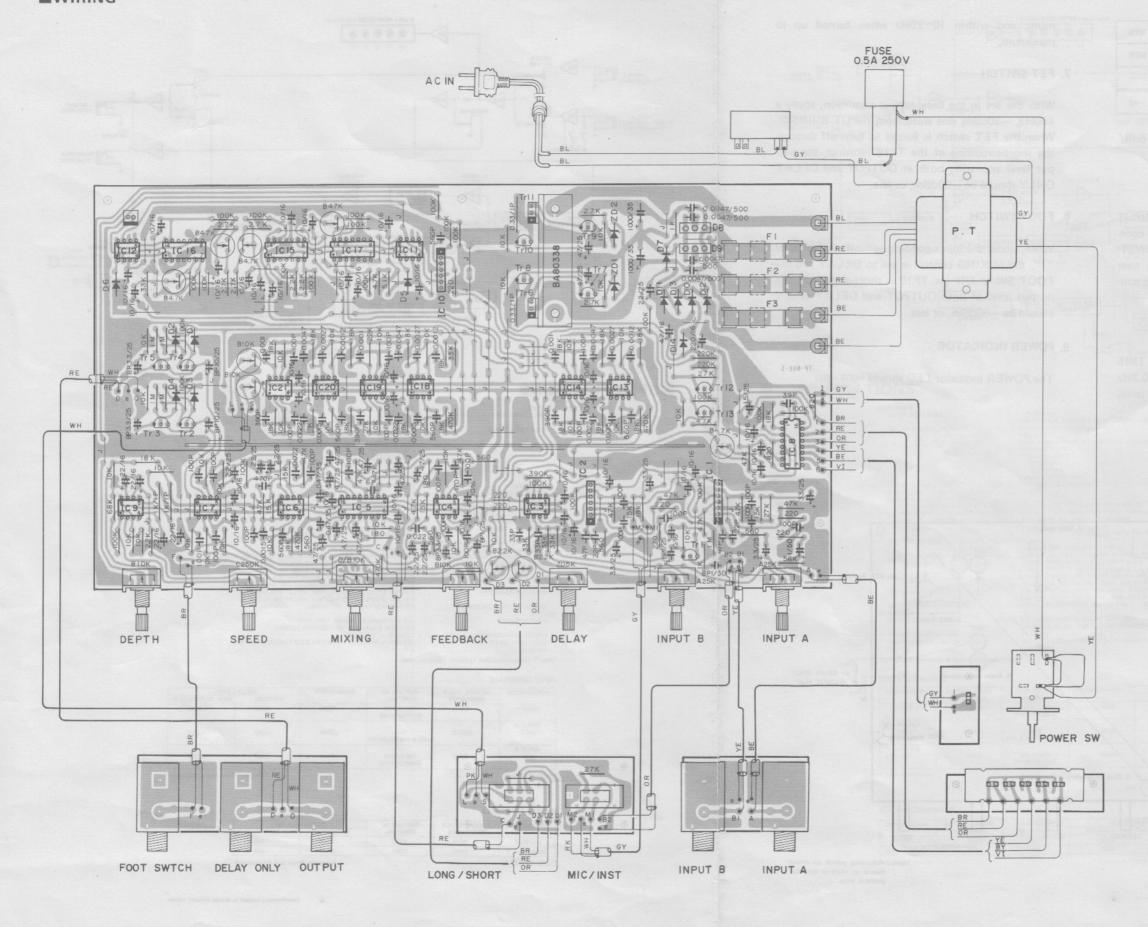
Terminals: Phone Jack (Unbalanced)

OUTPUT TERMINALS

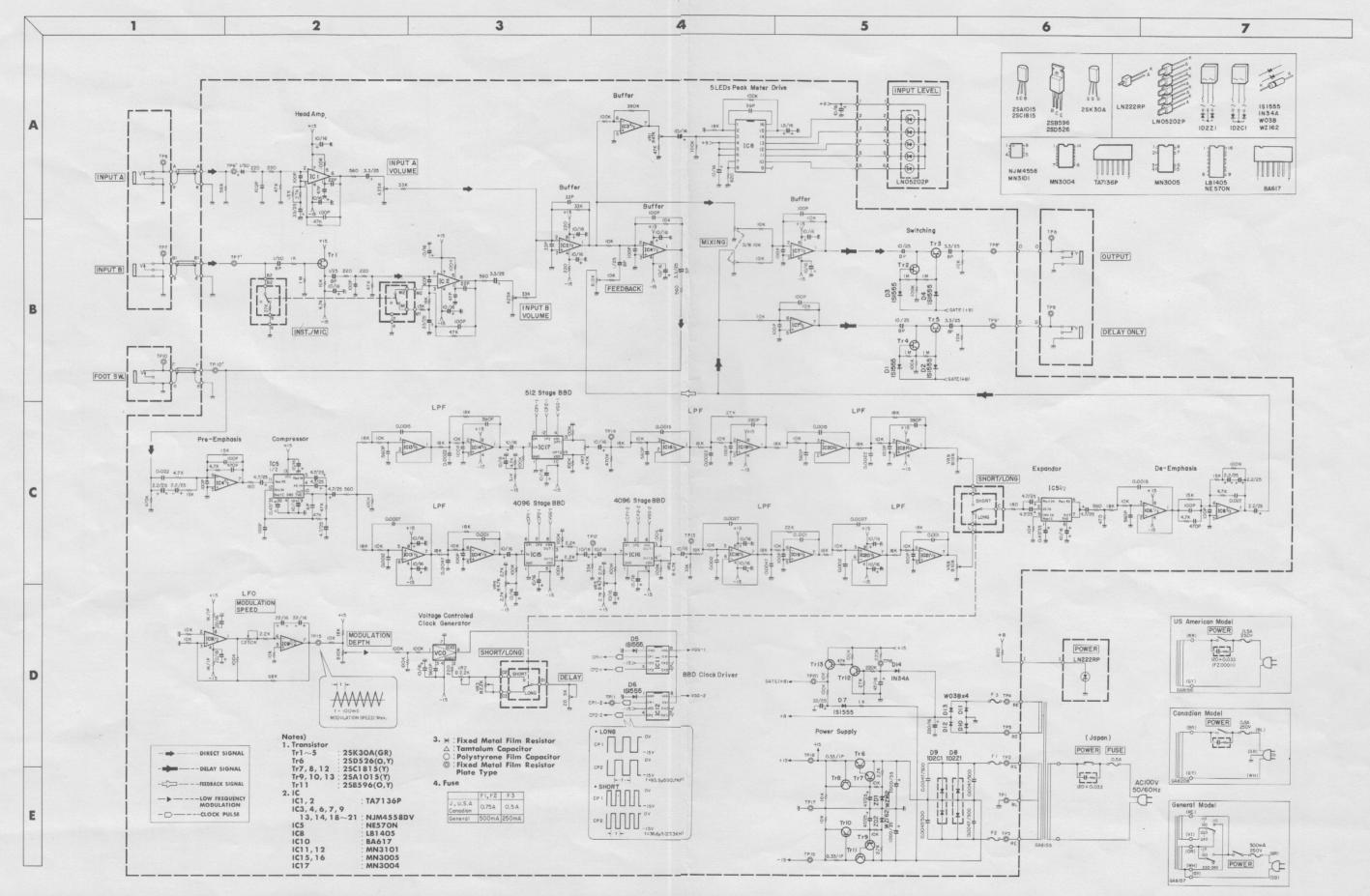
CONNECTION	ACTUAL SOUCE	FOR USE W/	OUTPUT LEVEL		
CONTRACTION	IMPEDANCE	NOMINAL	NOMINAL	MAX. BEFORE CLIP	
OUTPUT	250Ω	10kΩ	-20dB	-3dB	
DELAY ONLY	250Ω	10kΩ	-20dB	-3dB	

^{* 0}dB= 0.775Vrms

Terminals: Phone Jack (Unbalanced)

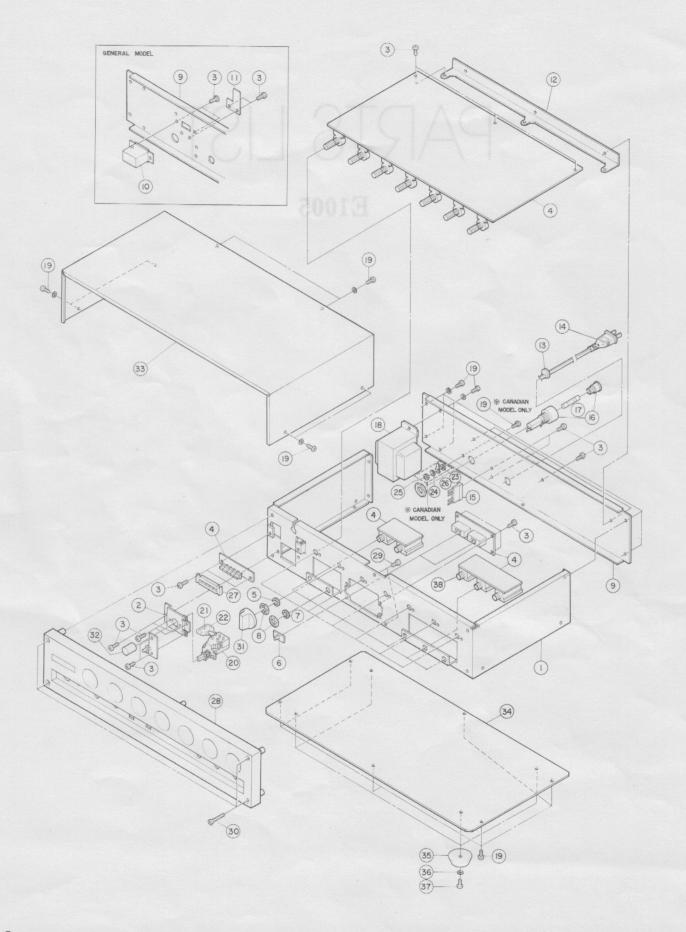


SCHEMATIC DIAGRAM



PARTS LIST

E1005



	Ref. No.	Part No.	Description	(部 品 名)	Remarks	Common	Markets
*	1	30 54 00 AA 81 16 00	Chassis #81160	シャーシー	5 50 T 00 B	118 A/R	130 MA OS
*	2	30 54 00 AA 81 20 40	Holder, P.SW	P.SW取付板	cuto8 D8 D	TR AST	120 54:61
	3	40 10 00 E D 33 00 60	Bind Screw M3×6 FCM3-B &	バインド小ネジ	Ball ST S	08 8 10	NO GROVE
	4	30 54 00 NA 80 54 80	FM Board 1/6~5/6	シート (FM)	J		io or oal
	11	30 54 00 NA 80 54 90	"	ll l	С		in at he
	11	30 54 00 NA 80 55 00	"	11	G		
	"	30 54 00 NA 80 57 70	"	"	U		
	5	40 10 00 E V 41 00 70	Toothed Lock A7S ZMC2-Y	歯付座金			
	6	30 10 00 CB 81 46 80		ツマミ	G 662 755 5	CS-40M	in ad or
	7	40 10 00 E V 41 00 90	Toothed Lock Age 7MC2 V	歯付座金	Ing a		
	8	40 10 00 L X 20 00 20	vvasilei	特殊六角ナット			
	9	30 54 00 AA 81 16 10		リアパネル	J	200 200	
	11	30 54 00 AA 81 16 20	" #81162	"	U		
	"	30 54 00 AA 81 16 30	" #81163	11	C	1 20 2 1 2	
	"	30 54 00 AA 81 16 40	" #81164	"	G		
1		40 10 00 KA 40 04 10		スライドSW	G		
	11	40 10 00 CB 81 42 50			G		
				ストッパー	3		
	12	30:54:00 A A:81:20:50		シートホルダー	J		
	13	40 10 00 CB 06 86 30		コードストッパー	C		
	"	40 10 00 CB 80 68 50		"			Marie DA
	11	40 10 00 CB 07 27 50	#07275	<i>"</i>	U,G		
	14	40 10 00 MG 00 06 00		電源コード	J	ACT SOUTH TO	
	11	40 10 00 MG 00 02 70	#00027	HOL II G SOMMER OF	C		32.53.59
		40 10 00 MG 00 08 90	# #00089 .	II .	U		
		40 10 00 MG 00 08 60	# # # # # # # # # # # # # # # # # # # #	"	G	100 21 10	D DE USI
	15	40 10 00 L A 00 07 60		カラー端子板	10018		O DE CE
	16	40 10 00 L B 20 04 90		ヒューズホルダー	J,U,C	18481	Q GITOP
	//	40 10 00 L B 20 05 90		II TOTAL BEAT BE	G	100	6) G.L. DEL
	17	40 10 00 K B 00 03 10		ヒューズ	J		
	11	40 10 00 K B 00 10 10	" UL(SS-2) 0.5A 250V	II .	U,C		E CENTRAL
	11	40 10 00 K B 00 07 10	" 500mA 250V	11	G	4 TREMA	
*	18	40 10 00 GA 81 55 00	Power Transformer	トランス	J	RESERVE OF	OCHON
*	11	40 10 00 GA 81 56 00	"	11	U	18 04 0	
(//	40 10 00 GA 81 57 00	II An	I II SE SOR POR	G	La Tivre K	
1	11	40 10 00 GA 82 08 00	II .	1 2 11	C	ROBA R	
	19	40 10 00 E D 34 00 80	Bind Screw M4×8 FCM3-B &	バインド小ネジ	- Fig. 9	0 0 0 0	0.01.06
	20	40 10 00 K A 80 02 20	Push Switch SDG5P	プッシュSW	J as	71 3	0.05.88
	//	40 10 00 K A 80 02 00	" SDG5P001	n .	U,C	1000	COLOR.
	11	40 10 00 K A 80 06 90	" SDG5P-E	11	G	(CO. 3 C	0 01 041
	21	40 10 00 F Z 00 01 10	Spark Killer Capacitor	スパークキラーコンデンサ	J,U		0.01.01
1	11	40 10 00 F Z 00 09 50	n .	assess n	С	00.9	0 01 04
1	22	40 10 00 C B 07 98 90	Cover for Capacitor	コンデンサカバー角型	С	00 3 6	a er ok
1	//	40 10 00 CB 07 21 90	II .	and the second second	J,U	00.34	0,61.04
1	23	40 10 00 E V 41 00 40	Toothed Lock Washer A4S ZMC2-Y	歯付座金	С	00 144	wat on
1	24	40 10 00 E V 30 00 40		バネ座金	С	00 8 8	0,01.08
1	25	40 10 00 E V 10 00 40	Hexagonal Nut M4 ZMC2-Y	六角ナット	С	0000	g g a nai
1		40 10 00 L A 00 02 90		アースラグ	С		40 10 0
1		30 54 00 CB 81 43 10		LEDプレート	4 00 A		e or net
1		30 54 00 CB 81 43 00		パネル	4 00 S		o ar asi
1		40 10 00 Ei 34 01 00		バインドタッピングネジ	4 60 2		io criosi
1			Oval Head Screw M4×35 FCM3-Bℓ	丸皿小ネジ	u 00.a	00.0 10	0 07 08
1		30 54 00 C B 81 23 70		ツマミ		E1010	
		30 54 00 CB 06 65 10		プッシュボタン		010	

※ ※: New Part (新部品) J: Japan, G: General, U: U.S.A, C: Canadian

Ref. No.	Part No.	Descr	iption	(部 品 名)	Remarks	Common	Markets
33	30 54 00 AA 81 16 50	Top Cover	#81165	トップカバー	6 0GL Chara	111 3.4	30 AS 02 L
34	30 54 00 AA 81 20 60	Bottom Cover	#81206	底板	SOURT ONE	18.44	30,58,08
35	40 50 00 CB 80 12 70	Leg	FT-C-N 10-EM	ゴム脚	bniā 08 04	PM300	10:01:01
36	40 10 00 EV 20 30 40	Flat Washer	4S ZMC2-Bℓ	平座金	6 H3 08 A	00 AH	30 At 06
37	40 10 00 ED 34 01 00	Bind Screw	M4×10 ZMC2-Bℓ	バインド小ネジ	6 10E A	CB ARE	D. PE.CE
38	40 10 00 LB 10 05 00	Phone Jack		基板型ホーンジャック			MINN CE
					05.5	BEAR	A AR DEL
			金融标准 Y-S	DES STA	101.01		EL P. D.
	30 54 00 NA 80 54 80	FM Board	3 8 10 10 10 10 10	FMシート	J		10.01.02
	30 54 00 NA 80 54 90	n	《集政队》 7.3	11	С		
	30 54 00 NA 80 55 00	п	(素质器等)	11	G		E OF GR
	30 54 00 NA 80 57 70	n	1 # 1 - 1 0 T I	n la	U	118449	
	30 54 00 AA 03 99 10	Metal for P.C.B	The second	反り止め金具	4 05.8	16.441	IN SECTION
	30 54 00 BA 80 33 80	Heat Sink	1881	放熱板	e	E1010	NO EL CE
	30 54 00 CB 07 28 80	Isulation Bush	a 6311	絶縁ブッシュ		18 64	0 45 00
	30 54 00 CB 81 43 20	LED Holder	ETTEX LABOR	LEDホルダー		300 4 34	10.21.04
	40 10 00 EL 02 80 80	Sems Screw	2.6×8 ZMC2-Y	セムス小ネジ	and Dist		NOTES.
	40 10 00 E i 03 00 80	Bind Tapping Screw	3×8 ZMC2-Y	バインドタッピングネジ	andle 65 G		
	40 10 00 EV 41 00 30		sher 3S	歯付座金			
	40 10 00 FD 11 25 60			スチコン	TO BE BU		
	40 10 00 HL 31 23 30	Metal Oxide Film Resistor	0.330 1P	酸金抵抗			0.01.02
	40 10 00 HL 31 61 00		1K 1P	n n		00000	
	40 10 00 HS 31 09 10	Variable Resistor	D/B 10K	ロータリーボリウム	4 95.80	110.00	
	40 10 00 HS 31 04 90	"	Α25Κ Ω	"	n GB &	E1010	
	40 10 00 HS 31 05 00	11	B10KΩ	11	N 108 84	11	00 01 04
	40 10 00 HS 31 05 20	11	C250KQ	# VO tenent	Court 08-50	11	40.10101
	40 10 00 HS 31 05 30	11	ZD5KΩ	11	BOLE OF 14	11	00 01 014
	40 10 00 HT 41 00 30	Variable Resistor	B2.2KΩ	ソリッドボリウム		relia.	00 01 01
	40 10 00 HT 41 00 40	11	Β4.7ΚΩ	"	SAVE SILES		16.01.04
	40 10 00 HT 41 00 70	11	B10KQ	"	1. 1010		Pipe Series
	40 10 00 HT 41 01 40	"	B47KΩ	"	N 10 10	100 44	30 O (01)
	40 10 00 HU 57 64 70		r 4.7KΩ	金属皮膜抵抗	9160 Y 000 S	18.83	10.01
	40 10 00 HU 57 65 10		5.1ΚΩ	11	100,11	Trans.	00.01.01
	40 10 00 HW 79 52 20		220g 33mA	プレート抵抗	100.5	DE ACT	00 111 34
	40 10 00 i A 10 15 20		2SA1015(Y)	トランジスタ		13 40	00 01 184
	40 10 00 i B 05 96 30		2SB596(O,Y)	"	1-16 02.9	FAR. d N	02.01.04
	40 10 00 i C 18 15 20		2SC1815(Y)	"		39.36	90.67.06
	40 10 00 i D 05 26 30		2SD526(O,Y)	"		00.836	TO GET THE
	40 10 00 i E 00 00 20		2SK30A(GR)	FET TOTAL SECTION	100.00	-08 A 7	GE OF ORE
	40 10 00 i F 00 00 10		IN34A	ダイオード	15 and 151 11	60'5	00 01 001
	40 10 00 i F 00 00 40		1S1555	"	100 100 100	-03 5 %	00.01.01
	40 10 00 i F 00 06 50		WZ162	ツェナーダイオード	19020 -08 8	10.80	06-07-08
	40 10 00 i F 00 17 20		LN222RP	LED	108 1	10.80	20 VI (0)
	40 10 00 i F 00 17 30		LN05202P	n n		1243	00 01 08
	40 10 00 i G 00 12 20		TA7136P	IC	Pre Amp.	Casy 3	00 e 1 64
	40 10 00 i G 00 13 90		NJM4558 DV	"	OP. Amp	011	OF DETAIL
	40 10 00 i G 00 14 10		BA617	ıı tatıqısı	VCG	00.A.J	do un est
	40 10 00 i G 02 54 00		MN3004	" 041	BBD	E1010	004205
	40 10 00 i G 03 12 00		MN3005	"	BBD	"	00-49-051
	40 10 00 i G 03 13 00		NE570N	"	Compander	"	09 01 941
	40 10 00 i G 03 14 00		LB1405	"	Level	11	op o Foal
	40 10 00 i G 03 75 00		MN3101	"	CP CP		no all rec
	40 10 00 i H 00 02 80		1D2C1	ダイオード	Generator		

※:New Part (新部品)